

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Manrique J. Brenes; Yen T. Nguyen		
Assignee:	Cisco Technology, Inc.		
Title:	METHOD AND SYSTEM FOR REAL-TIME BIT ERROR RATIO DETERMINATION		
Serial No.:	10/773,705	Filing Date:	February 6, 2004
Examiner:	Stephen M. Baker	Group Art Unit:	2133
Docket No.:	CIS0209US	Confirmation No.	8310

Austin, Texas
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MAIL STOP RCE
COMMISSIONER FOR PATENTS
P.O.BOX 1450
ALEXANDRIA, VA 22313-1450

REQUEST FOR CONTINUED EXAMINATION (RCE)

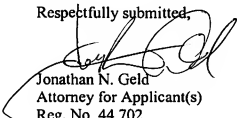
Dear Sir:

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application. Please consider and enter the Amendment & Remarks Accompanying RCE, which is being submitted with this request.

The RCE fee required under 37 C.F.R. § 1.17(e) is authorized. Applicants respectfully request issuance of a Notice of Allowance in light of the Amendment & Remarks which accompanies this RCE.

Please contact the undersigned attorney with any questions concerning this request or the above-identified patent application.

Respectfully submitted,



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